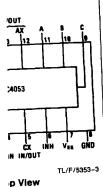
evice contains 6 together in pairs, multiplexer, or the ow configurations. pendently controls two switches to be





National Semiconductor

MM54HC4066/MM74HC4066 Quad Analog Switch

General Description

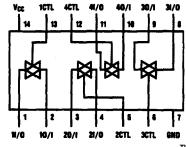
These devices are digitally controlled analog switches utilizing advanced silicon-gate CMOS technology. These switches have low "on" resistance and low "off" leakages. They are bidirectional switches, thus any analog input may be used as an output and visa-versa. Also the '4066 switches contain linearization circuitry which lowers the "on" resistance and increases switch linearity. The '4066 devices allow control of up to 12V (peak) analog signals with digital control signals of the same range. Each switch has its own control input which disables each switch when low. All analog inputs and outputs and digital inputs are protected from electrostatic damage by diodes to V_{CC} and ground.

Features

- Typical switch enable time: 15 ns
- Wide analog input voltage range: 0-12V
- Low "on" resistance: 30 typ. ('4066)
- Low quiescent current: 80 µA maximum (74HC)
- Matched switch characteristics
- Individual switch controls

Connection Diagram

Dual-In-Line Package



TL/F/535
Top View

Order Number MM54HC4066* or MM74HC4066*

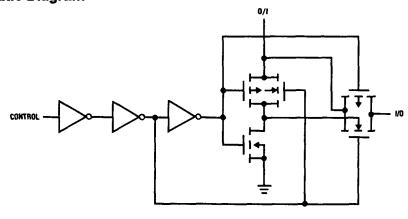
*Please look into Section 8, Appendix D for availability of various package types.

Truth Table

input	Switch
CTL	1/0-0/1
L	"OFF"
Н	"ON"

See the CMOS Logic Databook for Complete Specifications

Schematic Diagram



TL/F/5355-2